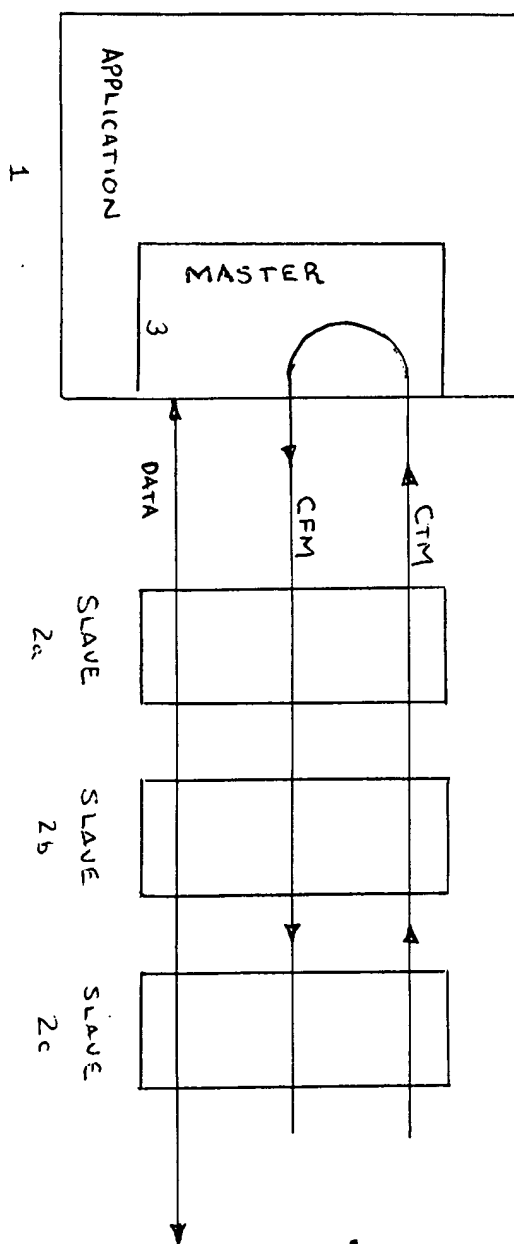
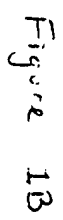


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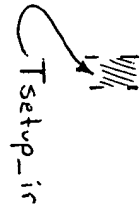
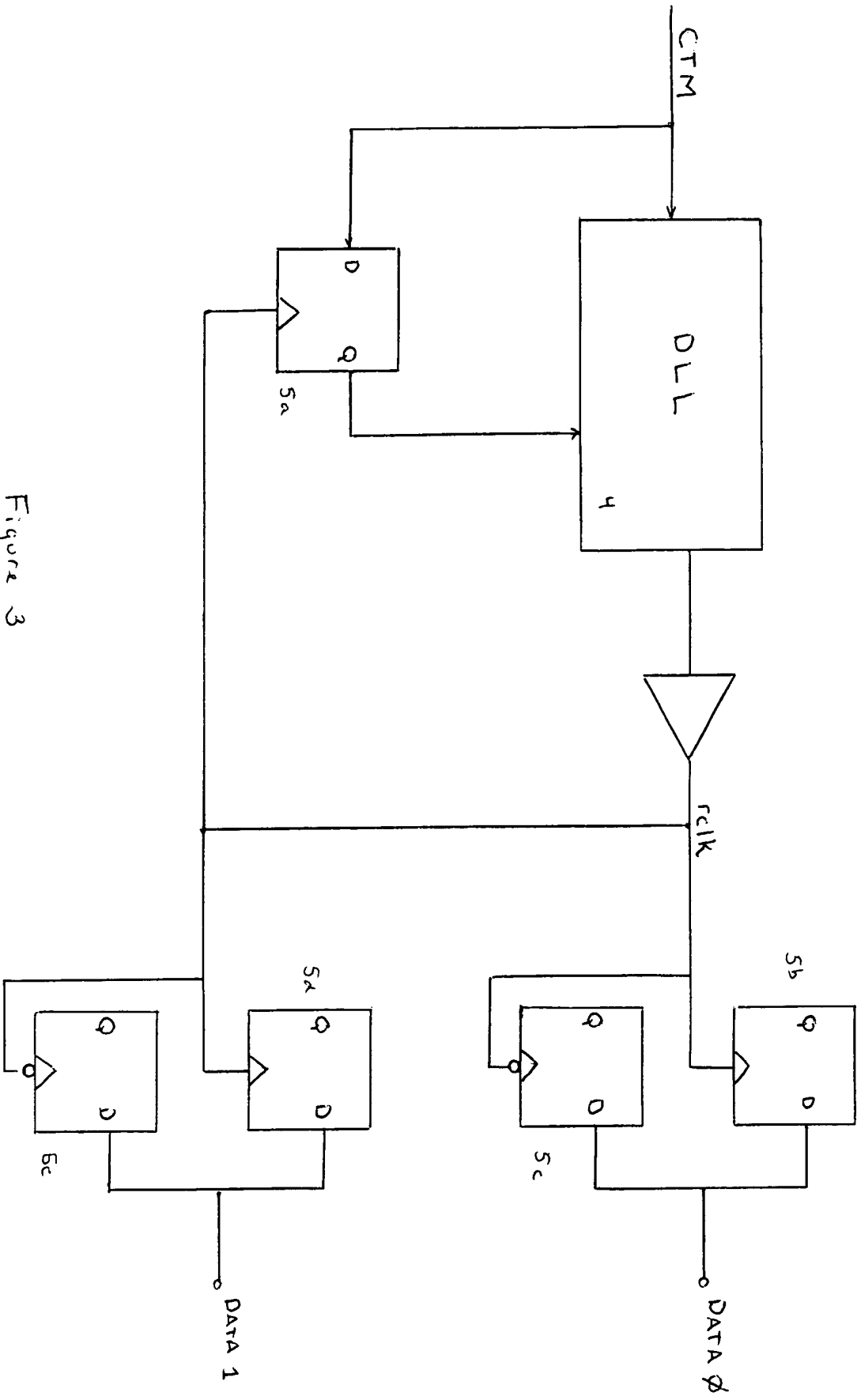


Figure 2



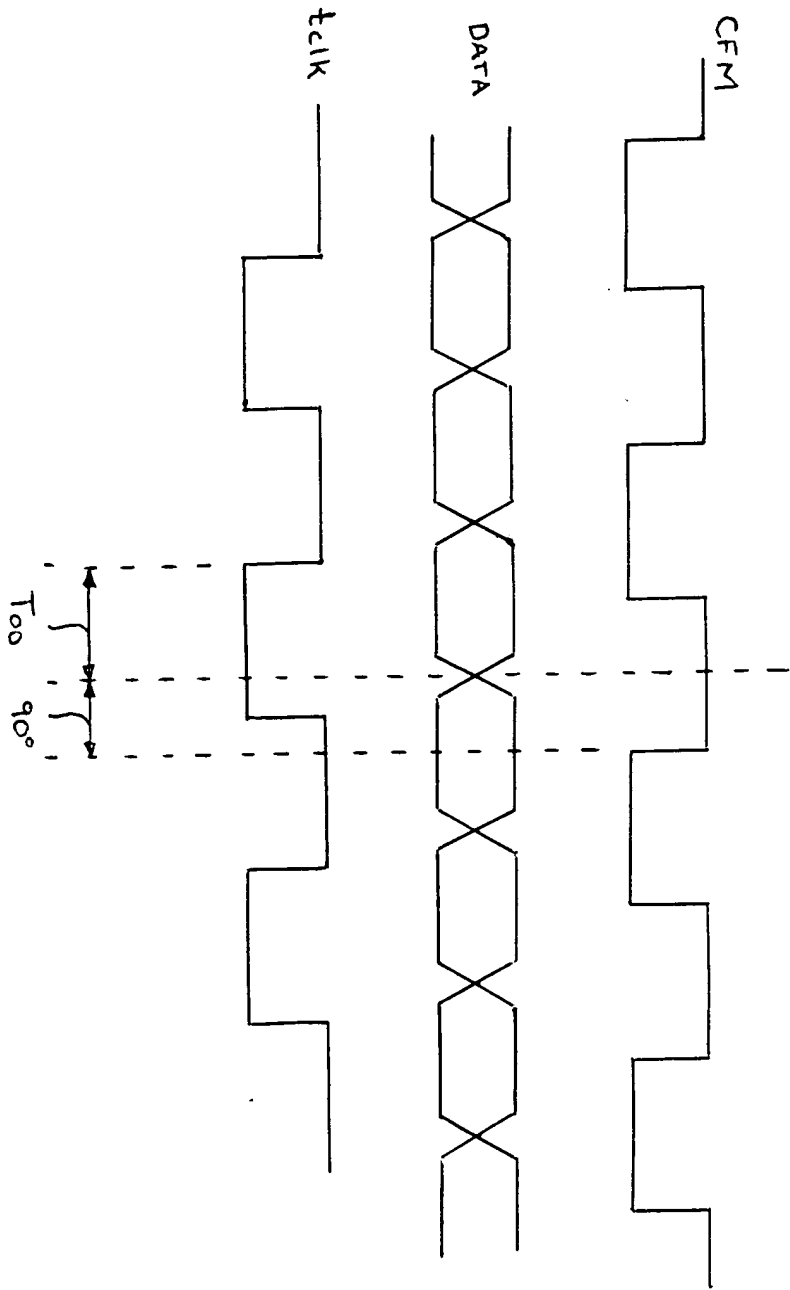


Figure 4

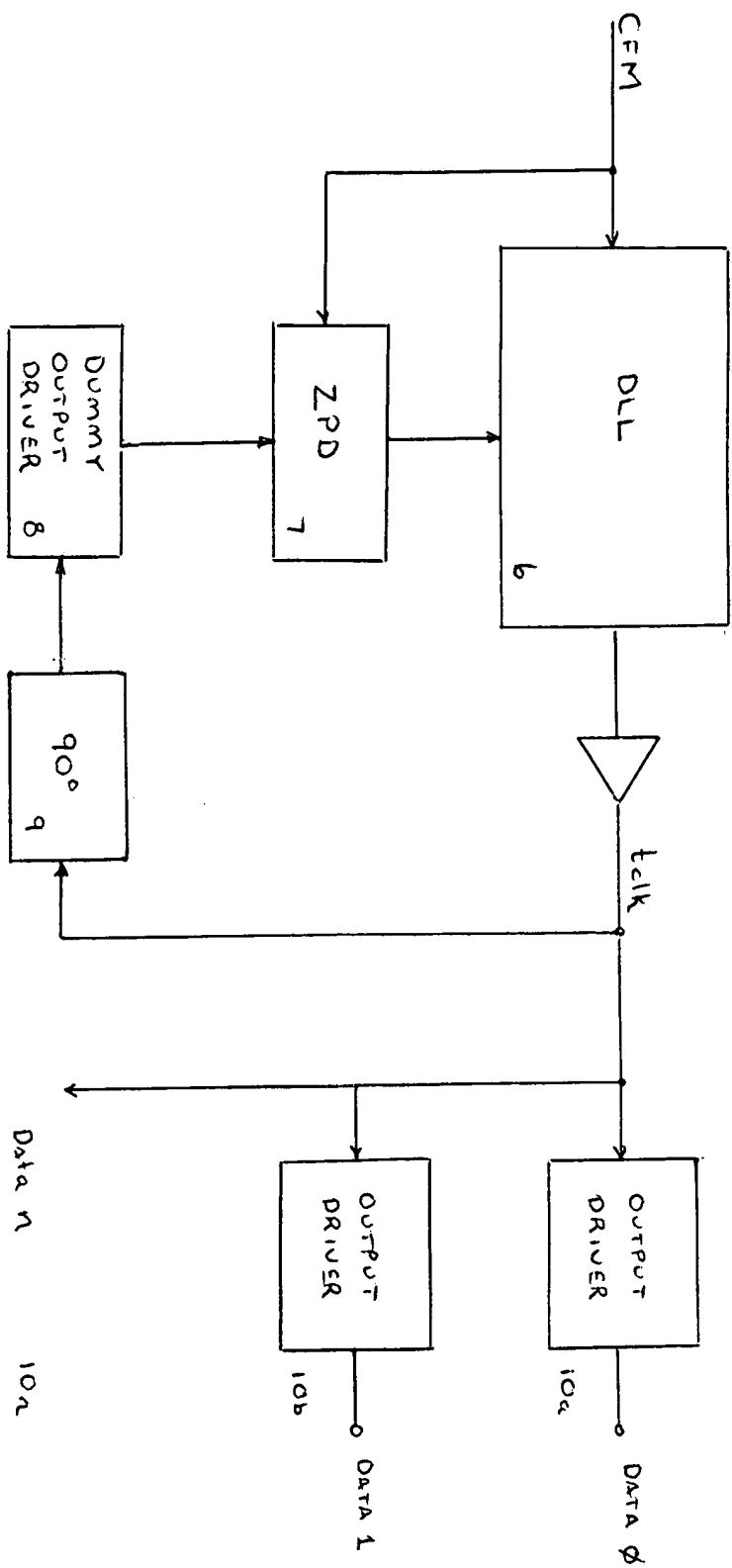


Figure 5

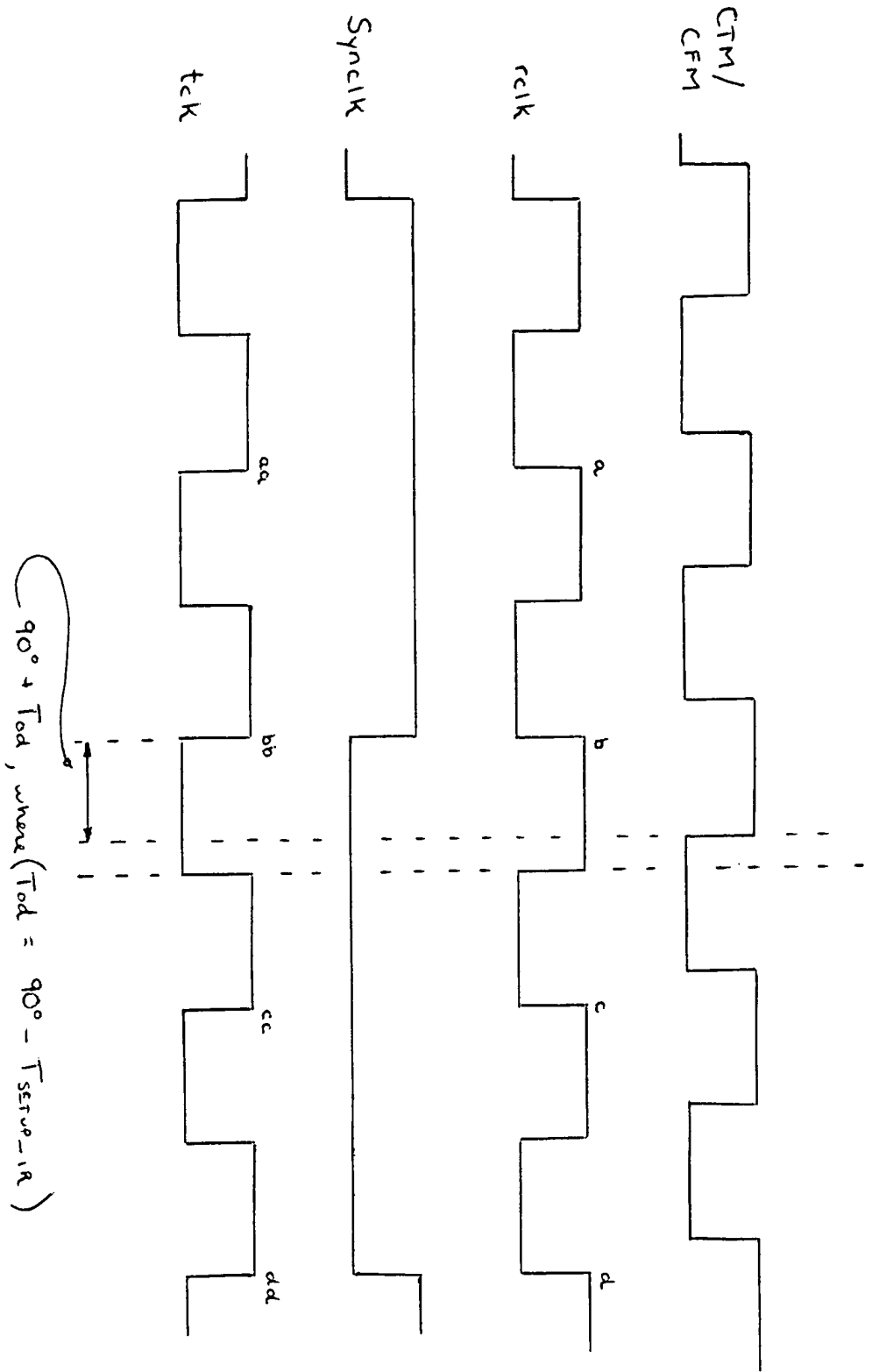


Figure 6

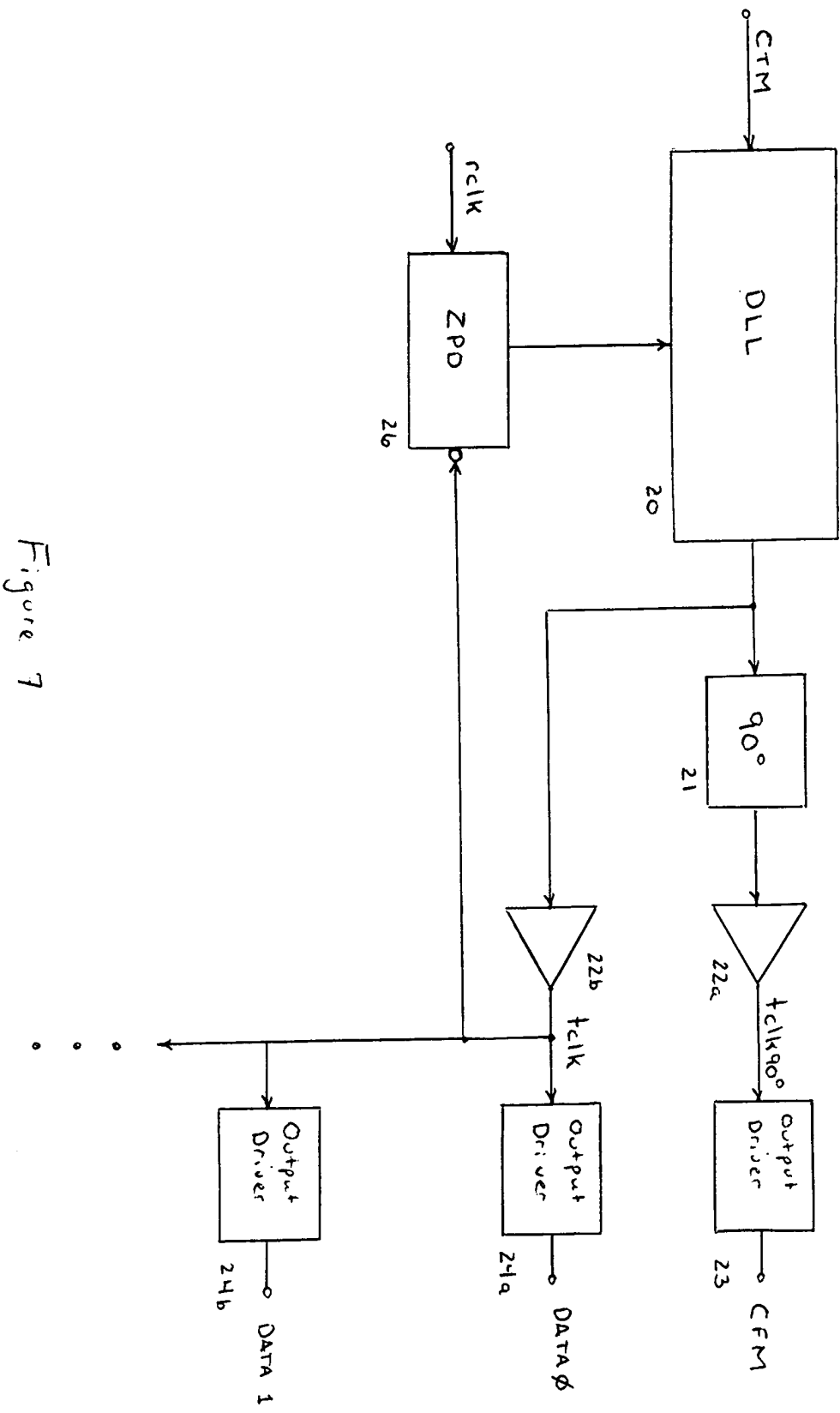


Figure 7



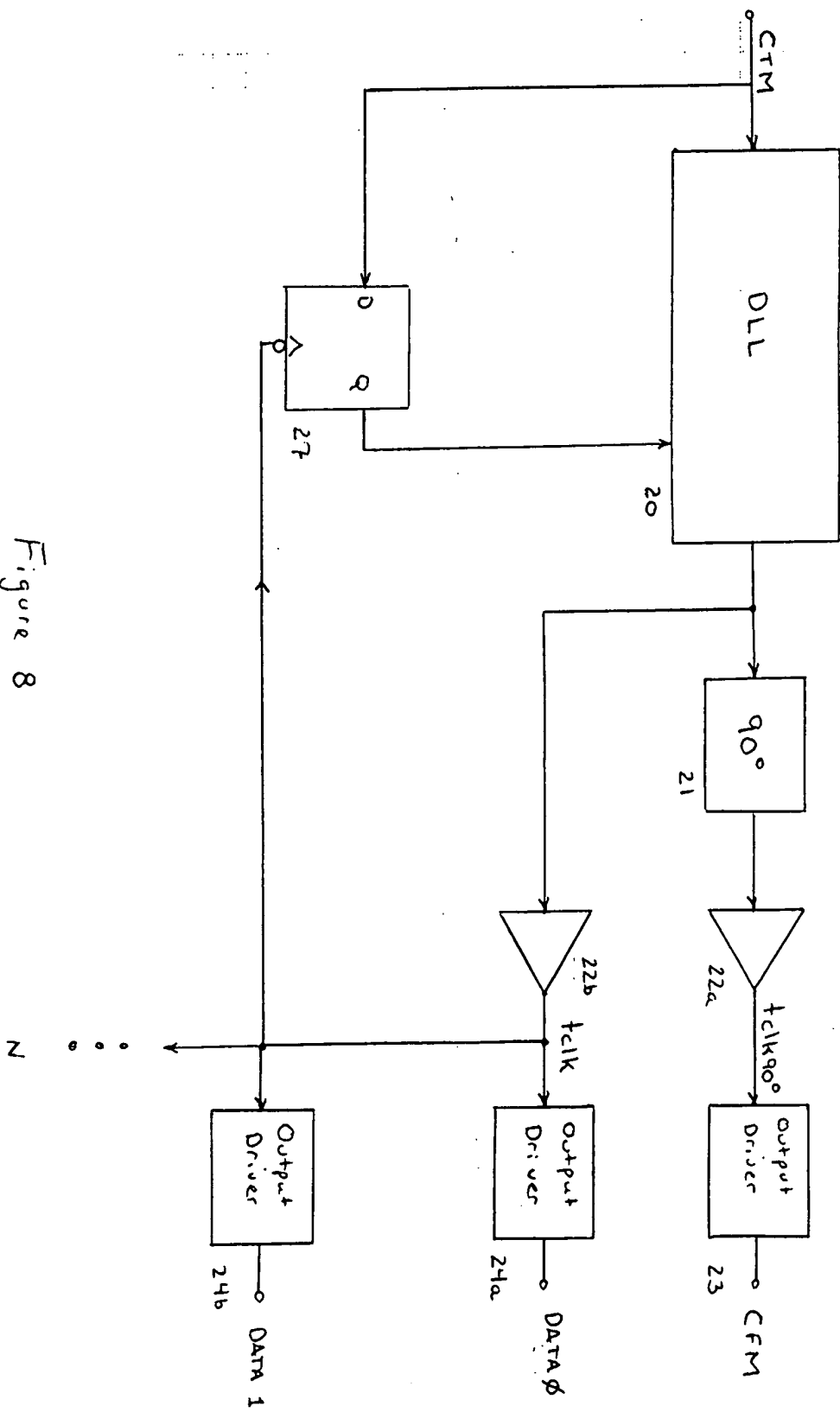
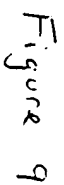


Figure 8

Input receiver setup time



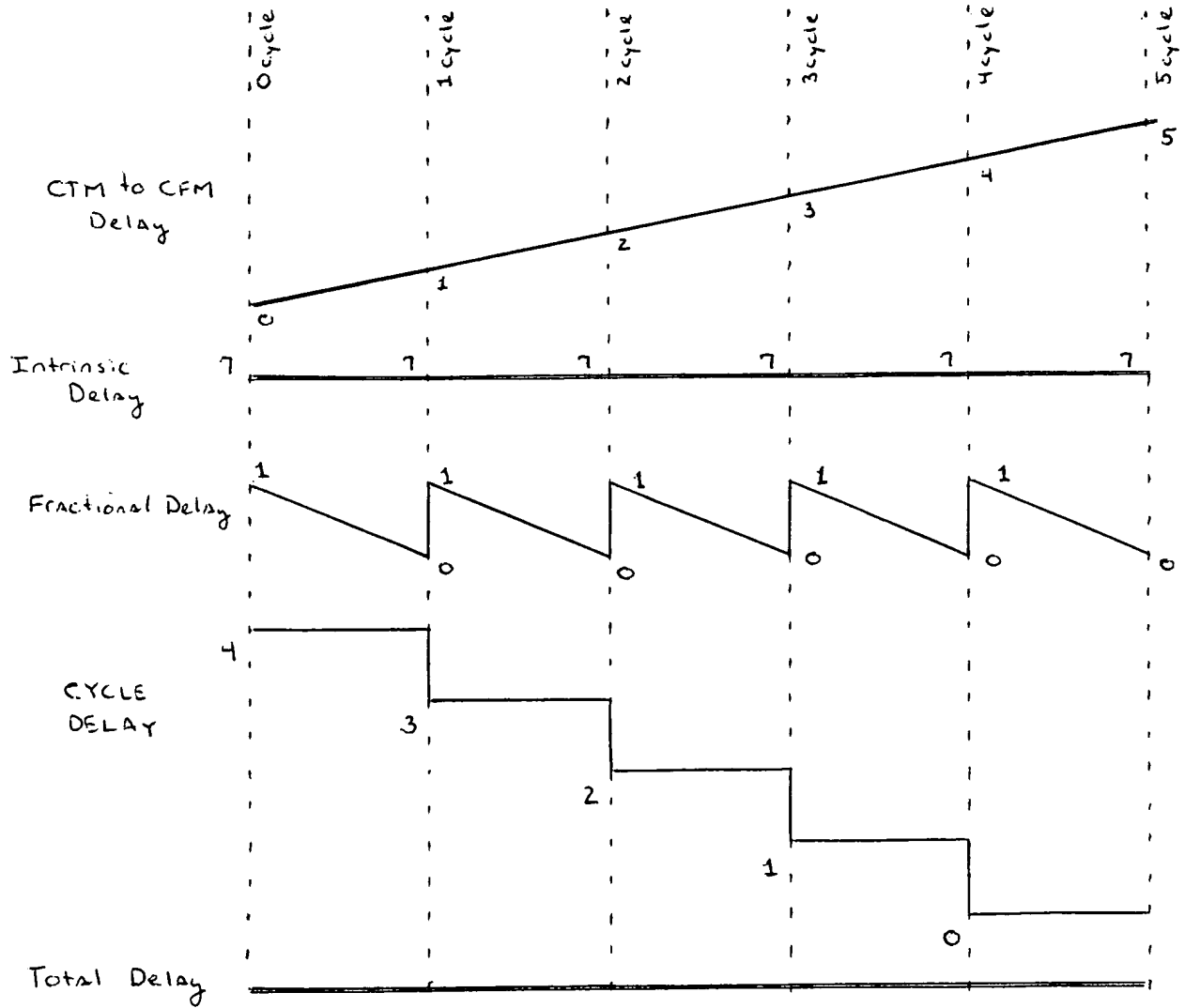
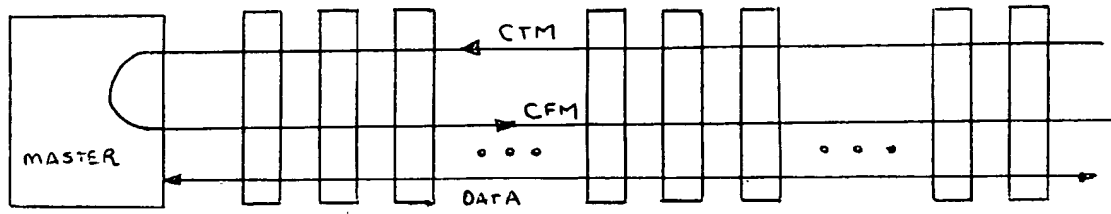


Figure 10

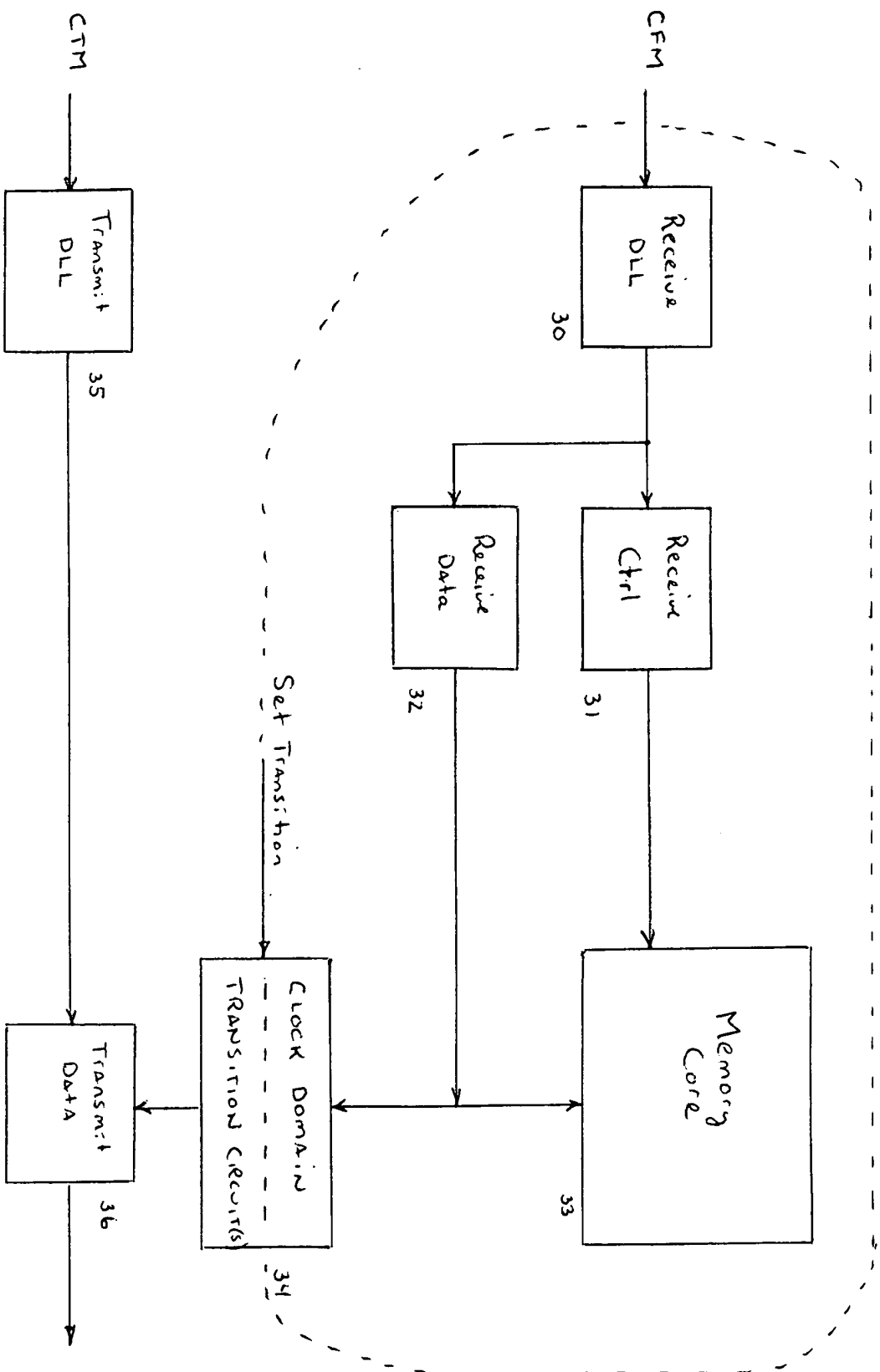


Figure 11



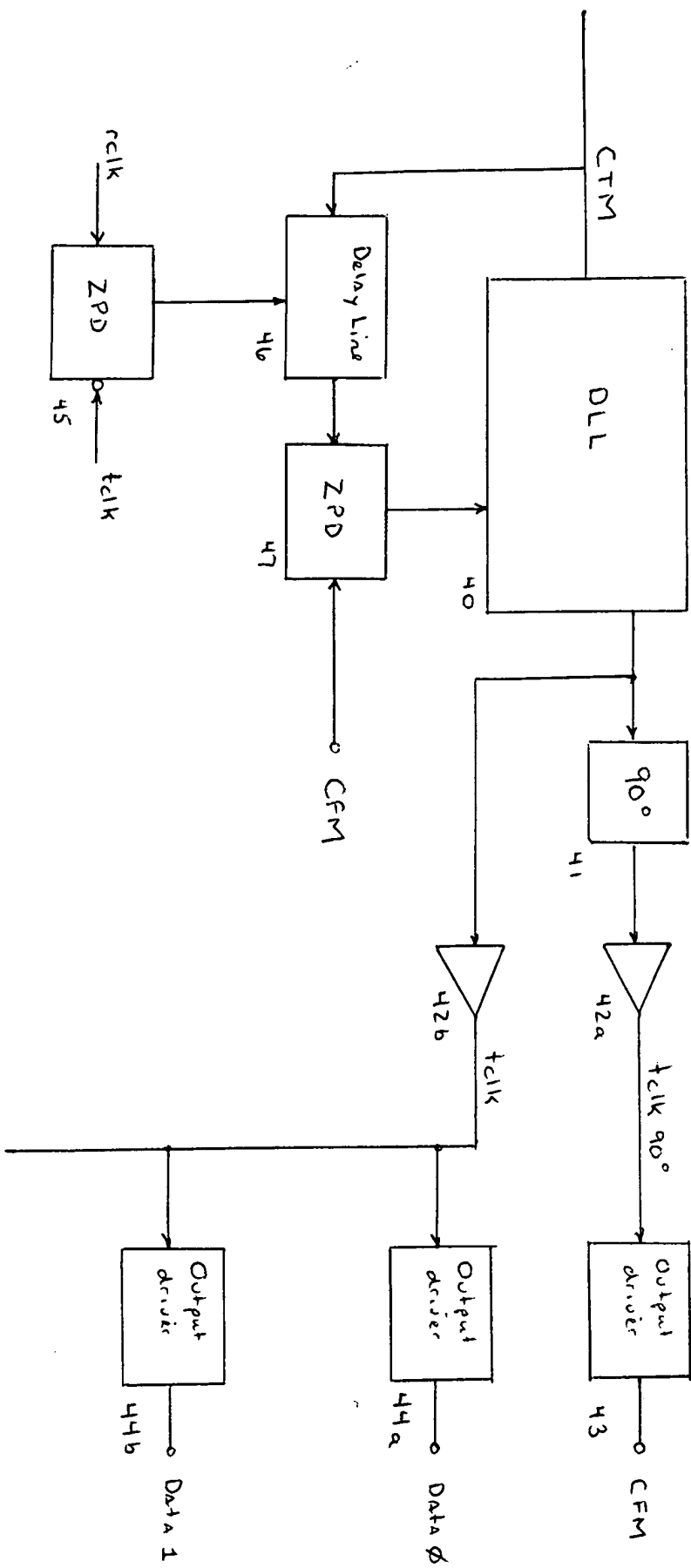


Figure 12